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		Application Number	10/530,267
		Filing Date	April 5, 2005
		First Named Inventor	Andrei Radulescu
		Art Unit	2111
		Examiner Name	
Sheet 1	of 1	Attorney Docket Number NL 021031	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
RP	1	KUMAR ET AL; "A NETWORK ON CHIP ARCHITECTURE AND DESIGN METHODOLOGY", VLSI ON ANNUAL SYMPOSIUM, IEEE COMPUTER SOCIETY ISVLSI 2002 PITTSBURGH, PA, 04/25/2002,	
RP		LOS ALAMITOS, CA.; PAGES 117-124, XP002263346.	

Examiner Signature	RAYMOND PHAN	Date Considered	2-20-07
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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